METHOD AND SYSTEM FOR JITTER CORRECTION
FIELD OF THE INVENTION

ABSTRACT OF THE DISCLOSURE

Jitter in parallel data streams is corrected for by generating one optimized clock

5   of 2N correction clock pulses for each received clock pulse from a phase locked loop.

Any one of the 2N correction clock pulses existing within a temporally overlapping

region of the bits may be used to latch bit values in the streams.

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